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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/617,100	07/14/2000	Koichi Sato	P19364	5280

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EXAMINER

GENCO, BRIAN C

ART UNIT PAPER NUMBER

2615

DATE MAILED: 03/17/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/617,100

Applicant(s)

SATO, KOICHI

Examiner

Eric D Wisdahl

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on ____.
- 2a) ☒ This action is FINAL. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-3 and 6-11 is/are pending in the application.
- 4a) Of the above claim(s) ____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) ____ is/are allowed.
- 6) ☒ Claim(s) 1-3 and 6-11 is/are rejected.
- 7) ☐ Claim(s) ____ is/are objected to.
- 8) ☐ Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 14 July 2000 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. ____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☐ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date ____.
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date ____.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: ____.

DETAILED ACTION

Response to Arguments

Applicant's arguments with respect to claims 1 – 3 and 6 – 11 have been considered but are moot in view of the new ground(s) of rejection.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 1 – 3 and 11 are rejected under 35 U.S.C. 103(a) as being unpatentable over Miyadera (U.S. Patent 5, 550, 587) in view of Nakano et al. (U.S. Patent 6, 094, 220).

Regarding Claim 1, Miyadera discloses a that calculates a white balance control amount for an electronic still camera, comprising:

- an image signal generator that generates an image signal corresponding to a first image formed on a light receiving surface of an imaging device (Column 3 lines 58 – 59);
- an image area extractor that extracts at least one image area, which contains a second image of a subject positioned at a predetermined distance from said electronic still camera, from said first image (Column 6 line 24 – Column 7 line 64, wherein a subject will have the same distance throughout the sectors it

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occupies and will obtain the same coefficients throughout its white balance adjustment); and

- a control amount calculator that calculates a control amount for performing a white balance adjustment, based on an image signal relating to said at least one image area (Column 6 line 24 – Column 7 line 64, Column 12 lines 39 – 47, see above);

Miyadera fails to specifically disclose the image area extracting processor, wherein:

- extracts a single image area, and
- said control amount calculating processor calculates said control amount, based on said image signal relating to said **single image** area which is extracted by said image area extracting processor.

Nakano teaches a method of:

- Extracting a single image area (Column 2 lines 46 – 53), and
- Calculating a control amount, based on said image signal relating to said single image area which is extracted by said image area extracting processor (Column 2 line 58 – Column 4 line 13).

Such an arrangement would be beneficial in maintaining the correct color information for an object of interest within a scene photographed.

Therefore, it would have been obvious to one of ordinary skill in the art to include the image area extracting processor wherein the processor extracts a single image area and the control amount calculating processor calculates the control amount based on the image signal relating to

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the single image area extracted by the image area extracting processor so as to maintain the correct color information for a subject of interest.

Regarding Claim 2, Nakano teaches a method of:

- Extracting a single image area (Column 2 lines 46 – 53), and
- Calculating a control amount, based on said image signal relating to said single image area which is extracted by said image area extracting processor (Column 2 line 58 – Column 4 line 13).

Regarding Claim 3, Miyadera and Nakano teach:

- second image is in-focus (Miyadera Column 10 lines 18 – 31), and
- said single image area is extracted by selecting pixels from said first image (Nakano Column 1 lines 42 – 47, Miyadera Column 6 line 24 – Column 7 line 64, Column 12 lines 39 – 47, see above),
- said pixel corresponding to points within a predetermined range from said predetermined distance (Miyadera Column 6 line 24 – Column 7 line 64, Column 10 lines 18 – 31, Column 12 lines 39 – 47, wherein for each “sector” it is seen that the object will be within the predetermined distance since the photographing lens is moved to the position wherein the object distance of the photographing lens is equal to the object distance from the object to the light receiving surface of the CCD, creating an in-focus state).

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Regarding Claim 11, Miyadera discloses a device which calculates a white balance control amount for an electronic still camera which photographs an image containing at least one subject and at least one portion other than said at least one subject, said white balance control amount calculating device comprising:

- an imager by which an image signal corresponding to said image is generated (Column 3 lines 58 – 59);
- an image area extractor that extracts at least one image area, each containing one of said at least one subject, from said image (Column 6 line 24 – Column 7 line 64, Column 12 lines 39 – 47, wherein a subject will have the same distance throughout the sectors it occupies and will obtain the same coefficients throughout it's white balance adjustment); and
- a control amount calculator that calculates a control amount for performing a white balance adjustment, based on said image signal relating to said at least one image area (Column 6 line 24 – Column 7 line 64, see above);

Miyadera fails to specifically disclose the image area extracting processor, wherein:

- extracts a single image area, and
- said control amount calculating processor calculates said control amount, based on said image signal relating to said **single image** area which is extracted by said image area extracting processor.

Nakano teaches a method of:

- Extracting a single image area (Column 2 lines 46 – 53), and

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- Calculating a control amount, based on said image signal relating to said single image area which is extracted by said image area extracting processor (Column 2 line 58 – Column 4 line 13).

Such an arrangement would be beneficial in maintaining the correct color information for an object of interest within a scene photographed.

Therefore, it would have been obvious to one of ordinary skill in the art to include the image area extracting processor wherein the processor extracts a single image are and the control amount calculating processor calculates the control amount based on the image signal relating to the single image area extracted by the image are extracting processor so as to maintain the correct color information for a subject of interest.

Claim 6 is rejected under 35 U.S.C. 103(a) as being unpatentable over Miyadera (U.S. Patent 5, 550, 587) in view of Nakano et al. (U.S. Patent 6, 094, 220) in further view of applicant's admitted prior art.

Regarding Claim 6, Miyadera fails to specifically disclose:

- control amount calculating processor extracts a third image composed of an achromatic color from said second image of said image area extracted by said image area extracting processor; and
- calculates said control amount based on only said third image.

Applicant's admitted prior art teaches:

- control amount calculating processor extracts an image composed of an achromatic color from said the image (Page 1 lines 11 – 15); and

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- calculates said control amount based on only said extracted image based on the achromatic color (Page 1 lines 11 – 15).

Such an arrangement of using the achromatic signals is well known and would be beneficial in accurately and efficiently correcting the color balance.

Therefore, it would have been obvious to one of ordinary skill in the art to include the control amount calculating processor extracts a third image composed of an achromatic color from said second image of said image area extracted by said image area extracting processor; and calculate said control amount based on only said third image so as to accurately and efficiently obtain a correct white balance color correction.

Claims 7 – 9 are rejected under 35 U.S.C. 103(a) as being unpatentable over Miyadera (U.S. Patent 5, 550, 587) in view of Nakano et al. (U.S. Patent 6, 094, 220) in further view of Malek (U.S. Patent 4, 915, 498)

Regarding Claim 7, Miyadera and Nakano fail to specifically disclose:

- image area extracting processor comprises a three dimensional image data sensing processor that senses three dimensional data indicating a distance from said electronic still camera to each point on a surface of said subject.

Malek teaches:

- a three dimensional image data sensing processor that senses three dimensional data indicating a distance from said electronic still camera to each point on a surface of said subject (Column 7 line 67 – Column 8 line 10);.

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Such an arrangement would be beneficial in providing accurate distance information which in turn would provide a more accurate white balance control.

Therefore, it would have been obvious to one of ordinary skill in the art to include the image area extracting processor comprising a three dimensional image data sensing processor that senses three dimensional data indicating a distance from said electronic still camera to each point on a surface of said subject so as to more accurately extract the image object and determine the correct white balance control.

Regarding Claim 8, Malek teaches:

- three dimensional image data sensing processor comprises a light source radiating a distance measuring light beam onto said subject (Column 7 lines 43 – 55), and
- three dimensional image signal generating processor that generates three dimensional data indicating a distance from said electronic still camera to each point on a surface of said subject (Column 7 line 67 – Column 8 line 10).

Regarding Claim 9, Malek teaches:

- three dimensional image signal generating processor comprises said image signal generating processor (Figure 1 item 9).

Claim 10 is rejected under 35 U.S.C. 103(a) as being unpatentable over Miyadera (U.S. Patent 5, 550, 587) in view of Nakano et al. (U.S. Patent 6, 094, 220) in view of Malek (U.S.

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Patent 4, 915, 498) in view of Yahav et al. (U.S. Patent 6, 057, 909) in further view of Kaneko et al. (Japanese laid-open Patent number Hei-4-351074).

Regarding Claim 10, Malek teaches said three dimensional image signal generating processor comprises:

- a plurality of photoelectric conversion elements that receive a reflected light beam from said subject, so that signal charge corresponding to an amount of said received reflected light beam is accumulated in each of said photoelectric conversion elements (Column 7 line 67 – Column 8 line 10);

Miyadera discloses:

- an electric charge discharging processor that discharges unwanted charge accumulated in each of said photoelectric conversion elements, so that said accumulation of said signal charge is started in each of said photoelectric conversion elements (Column 10 lines 57 – 60);

Malek and Miyadera fail to specifically disclose:

- a signal charge holding unit disposed adjacent to each of said photoelectric conversion elements;
- a signal charge transfer processor that transfers said accumulated signal charge in said photoelectric conversion elements to said signal charge holding unit; and
- a signal charge integrating processor that drives said electric charge discharging processor and said signal charge transfer processor alternately, so that said signal charge is integrated in said signal charge holding unit.

Yahav teaches:

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- Signal charge integrating processor that outputs control pulse signals to drive the electric charge discharging processor and transfer processor alternately (Column 2 lines 39 – 49 and Column 3 lines 9 – 38);

Furthermore, Yahav teaches the use of an electronic reset/readout technique in lieu of the crystal modulator used (Column 2 lines 40 – 49). Kaneko teaches such a known method.

Specifically, Kaneko teaches:

- Signal charge holding unit disposed adjacent to each pixel (section [0021]);
- Signal charge transfer processor (section [0021]);
- Electric charge discharging processor (section [0021]).

It is noted that the dynamic range of the image sensor is controlled on the basis of determining the range the object of interest. It is necessary that the sensor be within an operable range to prevent saturation (Malek Column 9 lines 8 – 16). Such an arrangement of including an electronic reset/readout technique utilizing a method in which a light source and the reflected light incident upon the image sensor are blocked and unblocked many times during a single field would be useful in increasing the signal to noise ratio of the image produced by the camera.

Therefore, it would have been obvious to one of ordinary skill in the art to include Signal charge holding unit disposed adjacent to each pixel, Signal charge transfer processor, Signal charge integrating processor that outputs control pulse signals to drive the electric charge discharging processor and transfer processor alternately in accordance with a number of control pulse signals, Signal level adjusting processor that controls the number of control pulse signals to adjust a level of said signal charge so as to produce an object distance image with an appropriate

dynamic range having an increased signal to noise ratio to therefore adjust the object image's white balance.

Conclusion

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than **SIX MONTHS** from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Brian C. Genco who can be reached by phone at 703-305-7881 or by fax at 703-746-8325. The examiner can normally be reached on Monday thru Friday 8:30am to 4:30 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Andrew Christensen can be reached on 703-308-9644. The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the customer service office whose telephone number is 703-308-4357.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Brian C Genco
Examiner
Art Unit 2615

March 16, 2004



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